

**EAST**

File View Edit Tools Window Help

Untitled.wpt

Drafts  
BRS:  
Pending  
Active  
L1: (60402) phase near2 generat\$3  
L2: (200140) phase\$1 near2 (signal\$1 or generat\$3)  
L3: (217443) (burst or phase\$1) near2 (signal\$1 or generat\$3)  
L4: (4221) 3 adj5 (gate\$1 or fetch\$3)  
L5: (692) conver\$3 adj2 burst\$1  
L6: (50) 4 and 5  
L7: (18) 4 adj5 (latch\$3 or conver\$3) and burst  
L8: (47) 4 adj5 (latch\$3 or conver\$3 or buffer or flipflop or flip) and burst  
L9: (24070) 2 near5 clock  
L10: (2136) (latch\$3 or conver\$3) near5 (clock near2 (clock or phase))  
L11: (1263) 9 and 10  
L12: (665) 9 with 10  
L13: (4518) (latch\$3 or conver\$3) near5 (clock near2 (clock or phase))  
L14: (691) 9 with 13  
L15: (3) 14 and 5  
L16: (824) 9 same 13  
L17: (4) 16 and 5  
L18: (129) 16 and burst

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15 and burst

#	Document ID	Issue Date	Pages	Title	Current CR	Current XRef	Prior retrieval C	Inventor	S	C	P	W	R
1	US 20030133527	20030717	25	Method and circuit for adjusting the timing of	375/354			Joo, Yangsung et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
	A1												
2	US 20030097535	20030522	32	High speed memory system	711/169			Hsu, Fu-Chieh et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
	A1												
3	US 20030076899	20030424	11	Polyphase channelization system	375/316			Kumar, Rajendra et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
	A1												
4	US 200300020830	20030130	13	Video signal converter for converting non-interlaced to	348/447	348/446		Sani, Mehdi H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
	A1												
5	US 20030005250	20030102	26	Method and system for adjusting the timing offset	711/167	711/104; 711/118		Johnson, Brian et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
	A1												

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File Edit Tools Window Help

File1.wpt1

L1: (130028) (synthesiz\$3 or combin\$3 or add\$3) near5 (burst or phase)  
L2: (596) 1 and 341/\$.cccls.  
L3: (2173) 1 and ((internal or external or second) adj clock)  
L4: (70) 3 and 341/\$.cccls.  
L5: (4536) burst near6 clock  
L6: (166982) phase near2 signal\$1  
L7: (1398) 5 and 6  
L8: (418) 7 and 1  
L9: (4) 8 and 341/\$.cccls.  
L10: (2781) 6 adj5 gate\$1  
L11: (573) non adj burst  
L12: (5) 10 and 11  
L13: (41) 10 and 5  
L14: (19) 1 and 13  
L15: (28143) (phase or (inver\$3 adj phase)) adj5 (latch\$3 or converter\$1 or fetch\$3 or gate\$1)  
L16: (497) 15 adj5 (synthesiz\$3 or combin\$3 or add\$3)  
L17: (5) 16 and 341/\$.cccls.  
L18: (33) 16 and burst

Failed

U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	A	
1	<input type="checkbox"/>	US 20030185326	20031002	25	Frequency drift and phase error compensation in a	375/371	375/226; 375/362;		Kolze, Thomas J.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input checked="" type="checkbox"/>	A1												
2	<input type="checkbox"/>	US 20020001451	20020103	29	Magnetic tape tracking control apparatus and	386/69	386/124		Yoshihiro, Toshitaka et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	<input checked="" type="checkbox"/>	A1			VTR signal processing circuit	386/21	386/17; 386/18		Kurihara, Takashi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
3	<input type="checkbox"/>	US 6330391	B1	20011211	13	Motor control system	310/688	318/254; 324/207.13;		England, James M. C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	US 6304014	B1	20011016	31	Bit synchronization circuit	375/360	327/141; 370/503;		Baba, Mitsuo et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	US 6278755	B1	20010821	24	Automatic frequency control circuit	375/344	455/192.2		Maruyama, Hidenori	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	US 6151371 A		20001121	8	Decompression processor for video applications	345/502	348/426.1; 348/721;		Purcell, Stephen C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	US 5815646 A		19980929	49	Decompression processor for video applications	382/236			Purcell, Stephen C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	US 5809174 A		19980915	113	Communication receiver using soft decision TCH/FACCH	375/229	375/316		Saezusa, Yasuhiro	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	US 5757851 A		19980526	24	Method and apparatus for mapping data of a	711/202	711/220		Purcell, Stephen C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	US 5608888 A		19970304	113	MPEG video decompression processor	382/232			Purcell, Stephen C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	US 5598483 A		19970128	48	Video camera apparatus including programmable	348/516	348/517; 348/522;		Ohtsubo, Hiroyasu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	US 5483290 A		19960109	31	Pal encoder using a digital signal processing	348/642	348/639; 348/640		Nikoh, Hidemitsu	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	<input type="checkbox"/>	US 5406335 A		19950411	15	Decompression processor for video applications	382/233	375/240.13; 375/240.15;		Purcell, Stephen C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14	<input type="checkbox"/>	US 5379356 A		19950103	93	Modem for fading digital channels affected by	375/232	329/304; 375/324;		Crutchlow, David N.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15	<input type="checkbox"/>	US 5311545 A		19940510	10									

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